

## **Initial Information Data Sheet**

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**Application Information**

<b>Title Line One::</b>	<b>ATOMIC LAYER DEPOSITION OF CAPACITOR</b>
<b>Title Line Two::</b>	<b>DIELECTRIC</b>
<b>Total Drawing Sheets::</b>	<b>5</b>
<b>Formal Drawings?::</b>	<b>Yes</b>
<b>Application Type::</b>	<b>Utility</b>
<b>Docket Number::</b>	<b>MIO 0082 N2/40509.292</b>

**Representative Information**

<b>Registration Number One::</b>	<b>26,397</b>
<b>Registration Number Two::</b>	<b>27,262</b>
<b>Registration Number Three::</b>	<b>29,001</b>
<b>Registration Number Four::</b>	<b>39,564</b>
<b>Registration Number Five::</b>	<b>38,769</b>
<b>Registration Number Six::</b>	<b>33,758</b>
<b>Registration Number Seven::</b>	<b>42,695</b>
<b>Registration Number Eight::</b>	<b>46,506</b>
<b>Registration Number Nine::</b>	<b>46,458</b>
<b>Registration Number Ten::</b>	<b>53,657</b>

**Continuity Information**

<b>This application is a::</b>	<b>Continuation of</b>
<b>&gt; Application One::</b>	<b>10/228,911</b>
<b>Filing Date::</b>	<b>August 27, 2002</b>

<b>which is a::</b>	<b>Division of</b>
<b>&gt; Application Two::</b>	<b>09/994,547</b>
<b>Filing Date::</b>	<b>November 27, 2001</b>
<b>Patent Number::</b>	<b>6551893</b>